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Visual Development and Real-time Simulation of DSP Algorithms using Hypersignal RIDE/VAB tools and *TORNADO-6x* and *TORNADO-P6x* DSP Systems

Hyperception Inc has added support for *TORNADO-62/67* and *TORNADO-P62/P67/P6202/P6203* DSP systems from MicroLAB Systems.

The *TORNADO-6x* and *TORNADO-P6x* (TMS320C6201/6701/6202/6203-based) DSP boards are both now supported by the RIDE and VAB component-based DSP software development products. These boards provide ultra-high performance and are available with a variety of daughter modules to address a variety of signal processing requirements.

The *TORNADO-P62/P67/P6202/P6203* boards are PCI-bus DSP boards that provide up to 1Mx32 SBSRAM, 8Mx32 SDRAM, up to 1Mx8 Flash, and 64Kx32 DPRAM memory. All these boards provide a parallel I/O and a serial I/O expansion site, as well as a host PCI-bus interface.

The *TORNADO-62/67* boards are ISA-bus DSP boards that provide up to 1Mx32 1/2x CPU clock SBSRAM, and 1Mx8 Flash memory. Both these boards provide a parallel I/O and a serial I/O expansion site, as well as host ISA-bus memory and I/O interfaces.

Integration of Hypersignal RIDE/VAB visual DSP algorithm development environments and of ultra-high performance *TORNADO-6x/P6x* DSP systems offers DSP development engineers an ultimate opportunity to design and simulate DSP+AD/DA algorithms in real-time using intuitive graphical user interface and signal processing blocks. The designed and debugged algorithm can be further converted into C-code for porting to any DSP platform.

Hyperception Inc.
www.hyperception.com

MicroLAB Systems Ltd
www.mlabsys.com